

Call for Papers for the Journal

International Journal of Embedded Systems and Computer Engineering (IJESC)

(Includes: Real Time, hardware, software, security, wireless, computer engineering)

<http://www.serialspublications.com/journals1.asp?jid=72&jtype=1>

An embedded system is designed to perform reliably and securely with high accuracy and quality a dedicated function for specific applications. It has wide applications in automotive, consumer electronics, aero-space, medical, networks, office automation, Communication, industrial control and many others.

IJESC will publish articles in real time embedded systems hardware, software, algorithms, modeling, parallel/system on a chip architecture, design methods, tools, testing, novel applications and use of advanced technology.

Objective

Aim of IJESC is to be a bridge for communication between academics, practicing engineers, students and rapidly advancing technology. Publish articles on emerging and advancing technology, innovative research, novel industrial applications, consumer electronics, aero-space and automotive applications, standards, design tools, testing methods, government policy and related issues in real time embedded systems.

Publication will be in printed high quality paper, and on the web as on-line version. Digital copies will be available on the web (abstracts are free for access, full papers for purchase or free for subscribers). Web will have various search features. The editorial team assures a quick review of all the articles submitted within two months and assures high quality.

Topics Coverage

- Embedded System Hardware: DSP, FPGA, System on a chip(SoC)
- Real Time system issues
- Hardware/Software co-design
- Multiprocessor system
- Testing
- Standards and Policy Matters
- Modeling
- Performance evaluation
- Industrial Applications
- Novel Applications
- Military Embedded systems
- Networked embedded systems
- Discussion on various international Embedded System issues
- RFID, implementation and applications
- Quality and Reliability in ES
- Wireless Embedded system
- Low power systems design
- MEMS, micro electromechanical systems
- Embedded System in Instrumentation
- Job openings/advertisements
- Educational issues
- New Products Review and future conference announcements

INFORMATION FOR AUTHOURS

<http://www.serialspublications.com/journals1.asp?jid=72&jtype=1>

1. High Quality articles so far unpublished in any journals and relevant to Real Time Embedded System are solicited
2. All papers should be sent to the Editor in Chief, IJRTES, by e-mail and regular mail (to ensure receipt and cross checking readability of figures). The manuscript sent by email should be in the form of a Pdf or word file, ZIP folders.
3. All the papers will be reviewed by two experts and the editor in chief. Authors will be notified of acceptance normally within two months.
4. The Editorial board has the right to reject any article if it is not suitable. Copyright for the published articles will be owned by the publisher.

MANUSCRIPT PREPARATION:

Submit papers of length 8 to 30 pages, typed in double space with times New Roman Font, size 12, double column on 81/2X11 inch paper size with one inch margins on all sides, and should include a 50-200 words abstract, 5-10 key words. To enable blind review, the names of the authors should be only on the first page. Add a 1 paragraph biography of the authors on the first page. (Sample paper to show format requirement will be available on the web site). The title should be brief to aid indexing. The articles should include an abstract introduction, sub-heading, illustrations conclusion and references. Figure should be legible and neatly drawn.

Copy-right form and revised manuscript need to be submitted for all the accepted papers, details of which will be sent after the review of the papers with reviewers' comments.

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