

# FINAL PROJECT

## Powering $(x^y)$ Architecture Using Hyperbolic CORDIC

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**Reconfigurable Computing**  
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# OUTLINE:

- ❖ Introduction
- ❖ Hyperbolic CORDIC
- ❖ Powering  $x^y$
- ❖ Powering  $x^y$  Implementation using Hyperbolic CORDIC
- ❖ AXI4, FIFOs and Power Interface
- ❖ Simulation
- ❖ Experimental Setup
- ❖ Results





## ❖ Introduction

- Powering is broadly used in wide range of fields like economics, biology, chemistry, physics, and computer science, with applications such as compound interest, population growth, chemical reaction kinetics, wave behavior, and public-key cryptography.
- Powering can be computed using hyperbolic CORDIC which can be build using shifters adders and LUTs.
- The high cost of powering computation in addition to showing our deep understanding of problem solving and ability to design embedded systems design using FPGA were the motivations behind this project.



## EXPANDED HYPERBOLIC CORDIC

- Hyperbolic CORDIC is used to compute hyperbolic functions in efficient and fast way

- In the expanded CORDIC the index of iterations will be from  $-M$  to  $N$ , two sets of equation for positive and negative iterations.

- Iterations with  $i=4,13, \dots k, 3k+1$  must be repeated to guarantee convergence

$$i \leq 0 : \begin{cases} x_{i+1} = x_i + \delta_i y_i (1 - 2^{i-2}) \\ y_{i+1} = y_i + \delta_i x_i (1 - 2^{i-2}) \\ z_{i+1} = z_i - \delta_i \theta_i, \quad \theta_i = \text{Tanh}^{-1}(1 - 2^{i-2}) \end{cases}$$

$$i > 0 : \begin{cases} x_{i+1} = x_i + \delta_i y_i 2^{-i} \\ y_{i+1} = y_i + \delta_i x_i 2^{-i} \\ z_{i+1} = z_i - \delta_i \theta_i, \quad \theta_i = \text{Tanh}^{-1}(2^{-i}) \end{cases}$$

*Rotation:*  $\delta_i = -1$  if  $z_i < 0$ ;  $+1$ , otherwise

*Vectoring:*  $\delta_i = -1$  if  $x_i y_i \geq 0$ ;  $+1$ , otherwise



## EXPANDED HYPERBOLIC CORDIC

- $x_n, y_n, z_n$  converges to the following

$$\text{Rotating : } \begin{cases} x_n = A_n(x_0 \cosh z_0 + y_0 \sinh z_0) \\ y_n = A_n(y_0 \cosh z_0 + x_0 \sinh z_0) \\ z_n = 0 \end{cases}$$

$$\text{Vectoring : } \begin{cases} A_n \sqrt{x_{in}^2 + y_{in}^2} \\ y_n = 0 \\ z_n = z_{in} + \text{Tanh}^{-1} \frac{y_{in}}{x_{in}} \end{cases}$$

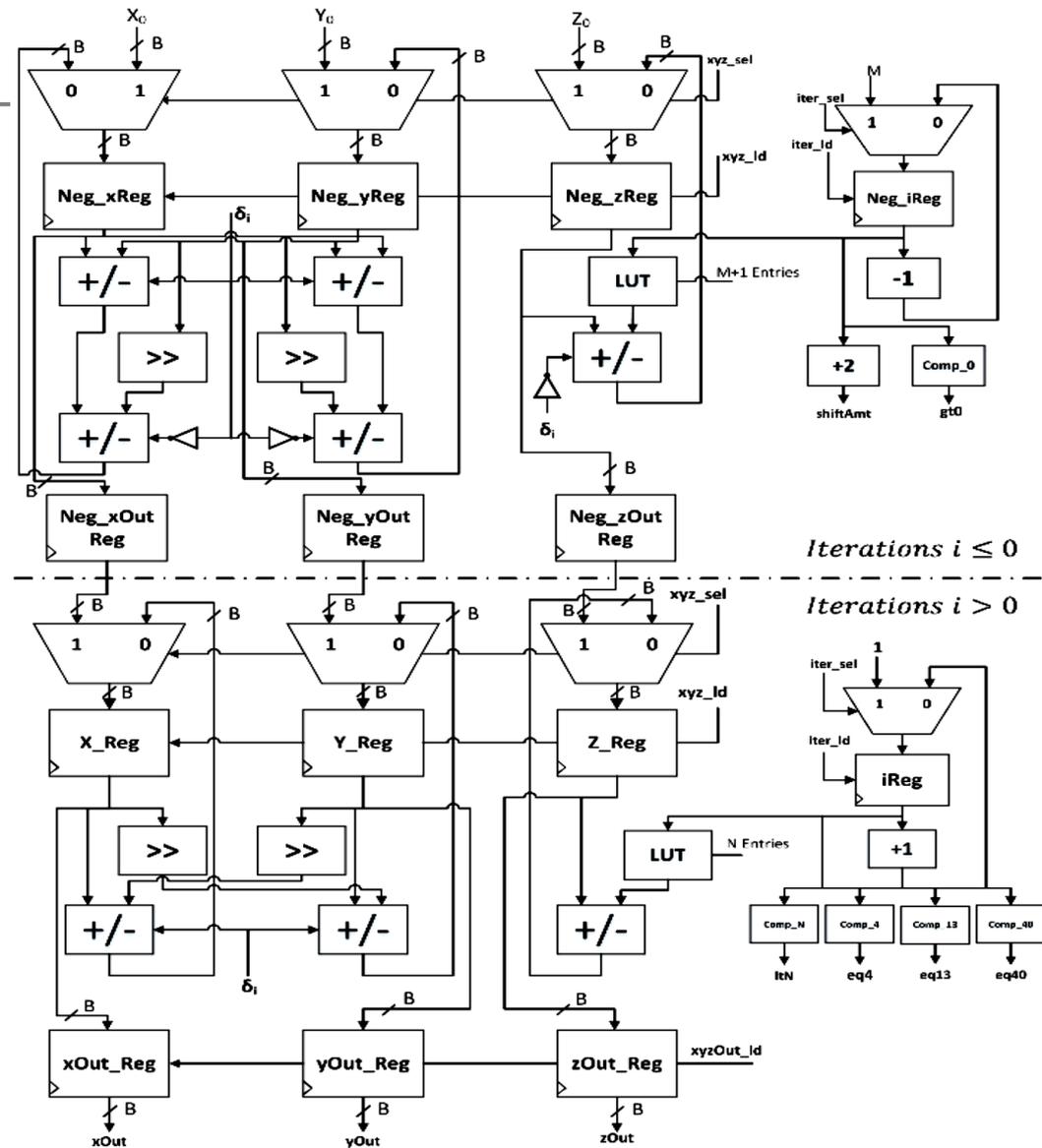
- Where  $A_n$  is defined as:

$$A_n = \left( \prod_i^0 = -M \sqrt{1 - (1 - 2^{i-2})^2} \right) \prod_i^N \sqrt{1 - 2^{-2i}}$$



## EXPANDED HYPERBOLIC CORDIC

- Two Stages
- FSM & LUT For each state
- B is 32 bit floating point format





## ❖ Powering $x^y$

- The goal is to compute  $x^y$ , which could be achieved by getting  $e^{y \ln x}$
- Two functions required  $\ln x$  and  $\exp(x)$

- $\ln x$  is defined as: 
$$\ln x = 2 \tanh^{-1} \frac{x-1}{x+1}$$

- By setting  $x_0 = x + 1, y_0 = x - 1, z_0 = 0$ , in vectoring mode the output  $z_n$  converges to  $\frac{1}{2} \ln x$

- $\exp(x)$  is defined as: 
$$e^x = \cosh x + \sinh x$$

- By setting  $x_0 = y_0 = \frac{1}{A_n}, z_0 = x$ , in rotating  $x_n$  converges to  $e^x$



## ❖ Powering $x^y$

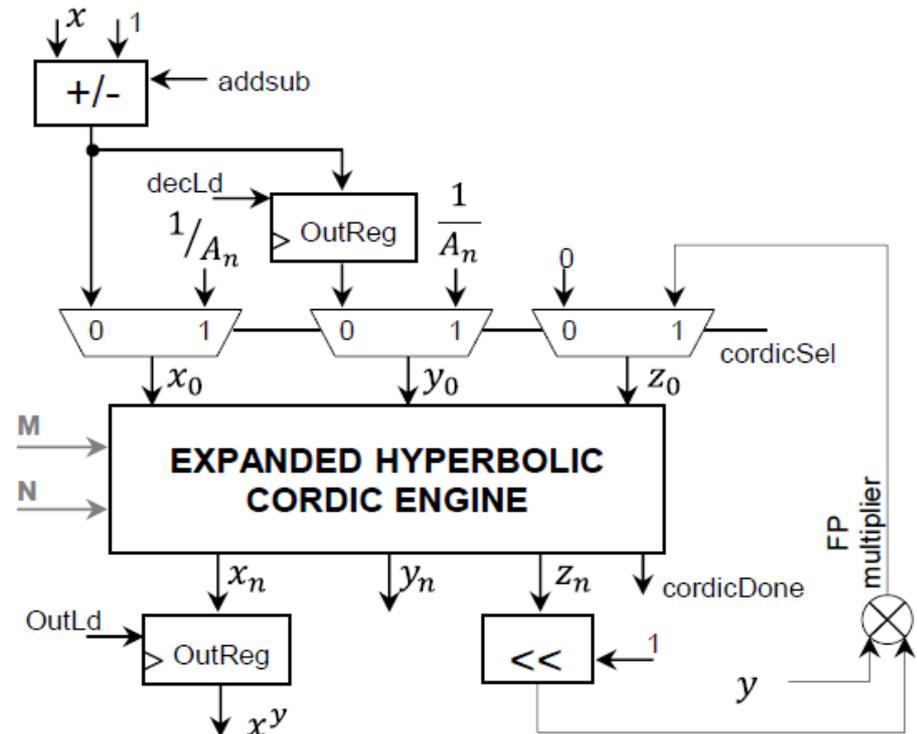
- Required input to get  $x^y$ 
  - By setting  $x_0 = x + 1, y_0 = x - 1, z_0 = 0$ , in vectoring mode the output  $z_n$  converges to  $\frac{1}{2} \ln x$
  - Multiply  $\frac{1}{2} \ln x$  by  $2y$  to  $y \ln x$
  - By setting  $x_0 = y_0 = \frac{1}{A_n}, z_0 = y \ln x$ , in rotating mode  $x_n$  converges to  $x^y$



## ❖ Powering $x^y$ Implementation using Hyperbolic CORDIC

Expanded CORDIC twice as in the following steps:

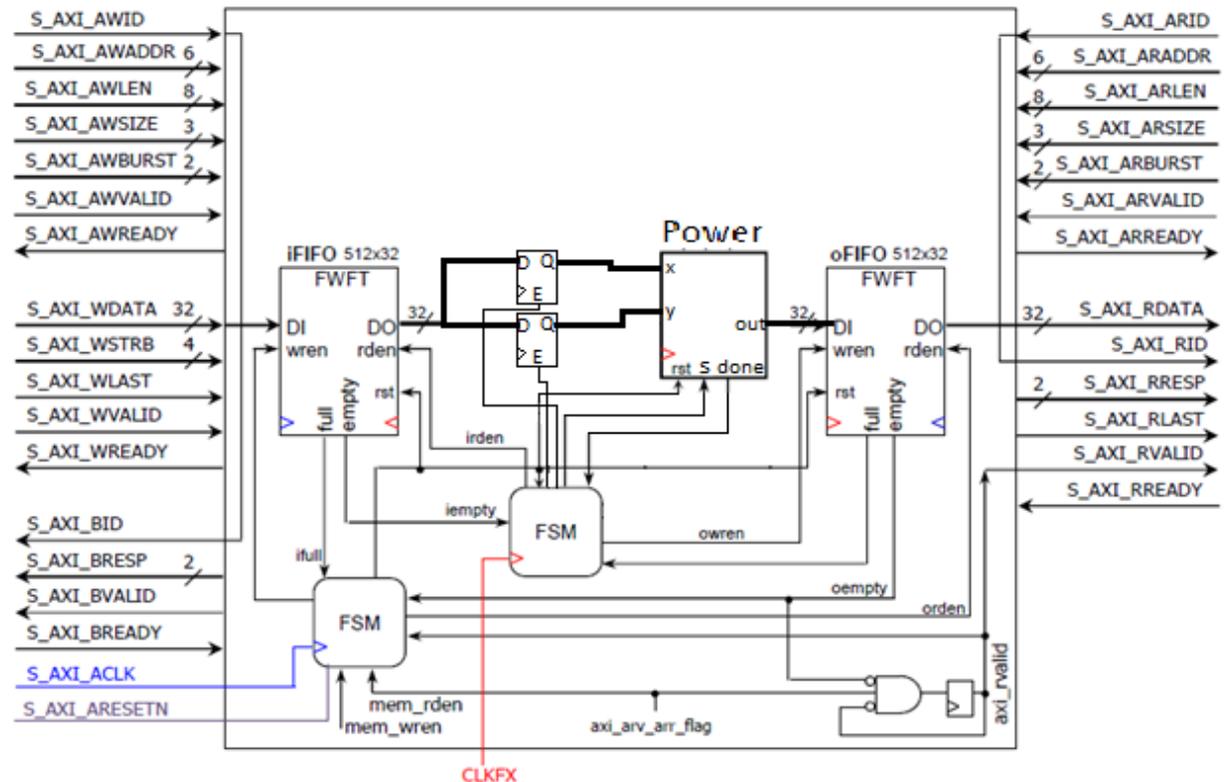
1. Use vectoring mode to get  $z_n = \ln x/2$  by loading  $x_0 = x + 1, y_0 = x - 1, z_0 = 0$ , this is done using the addsub unit on the top left corner and the multiplexers at the input stage.
2. To get  $y \ln x$ , first  $z_n$  is multiplied by 2 using the shifter, then the floating point multiplier is used to multiply  $2z_n$  output by  $y$  to get  $y \ln x$ .
3. Use the current output of  $z_n$  as the next input to the CORDIC block in rotating mode. With  $x_0 = y_0 = 1/A_n$ , this is again done using the multiplexers at the input stages. The output  $x_n = e^{y \ln x} = x^y$  is obtained in OutReg.





## AXI4, FIFO and Power Interface

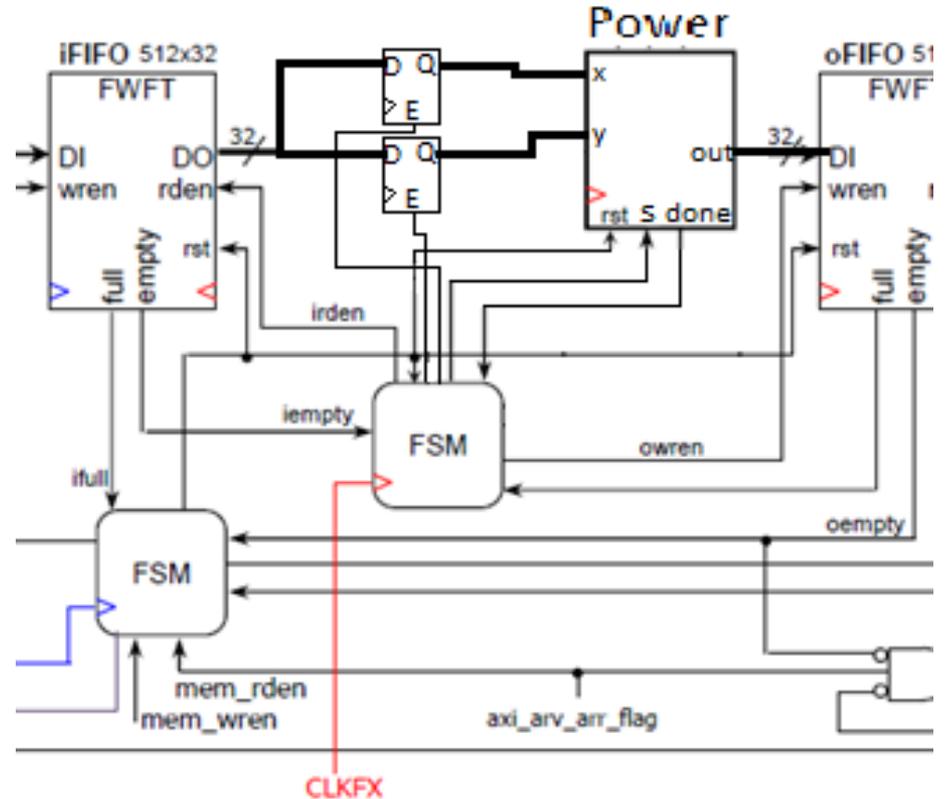
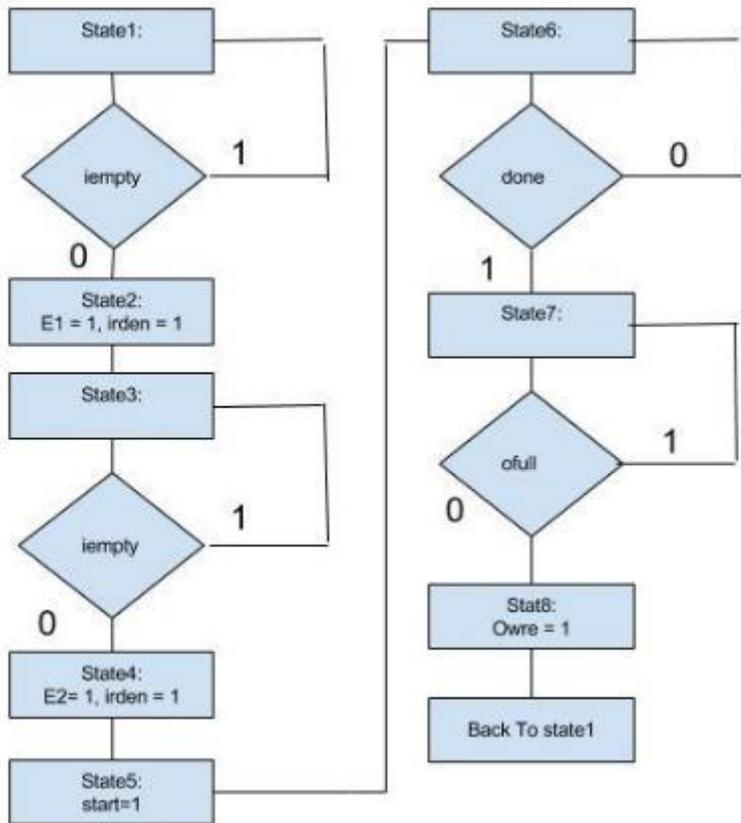
- We have two 32 bit words (x and y)
- FIFOs are used in the AXI interface
- After the FIFOs we do have registers to capture the data once its available.





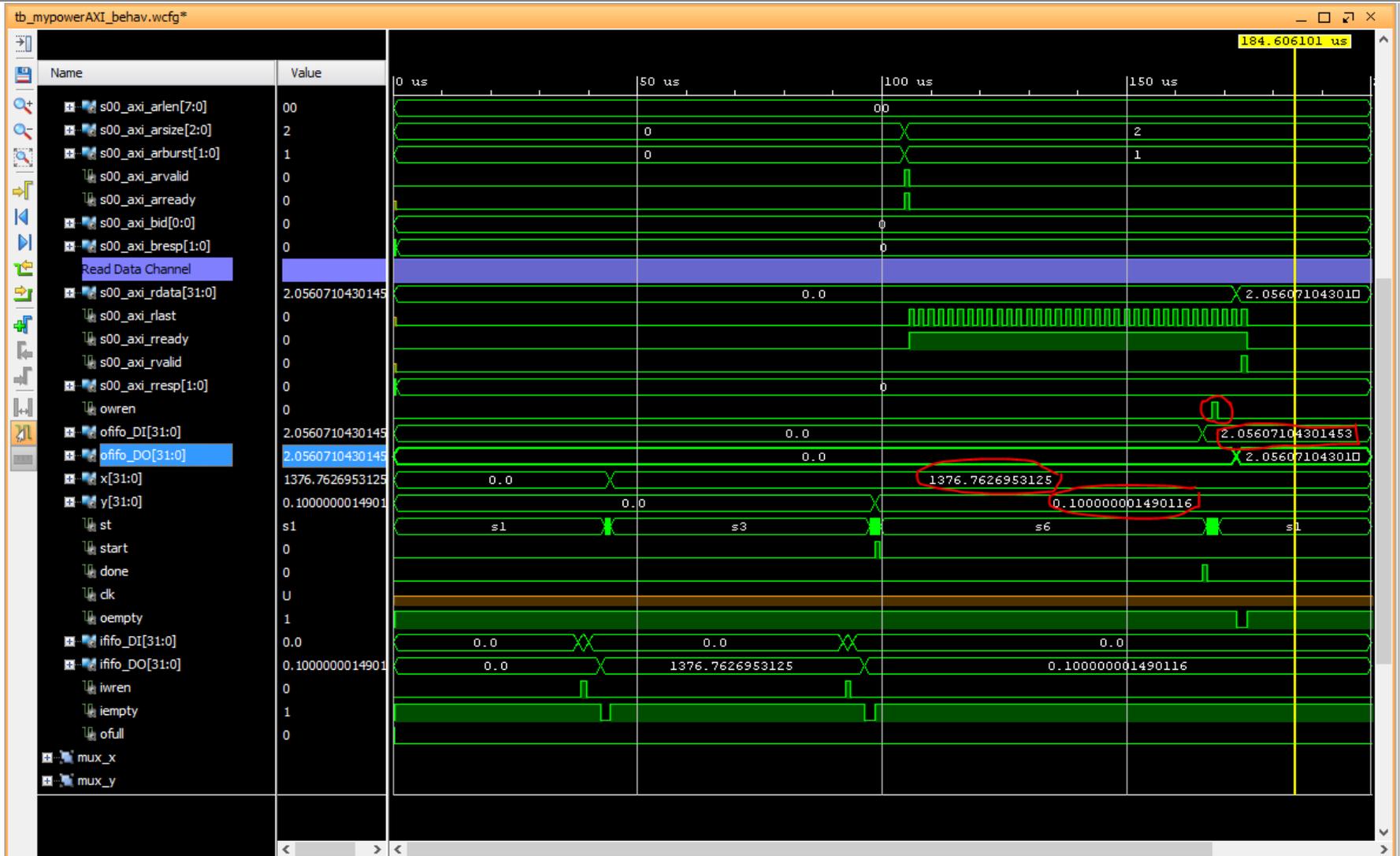
## AXI4, FIFO and Power Interface

- Blue FSM stay the same
- Red FSM was modified as follows



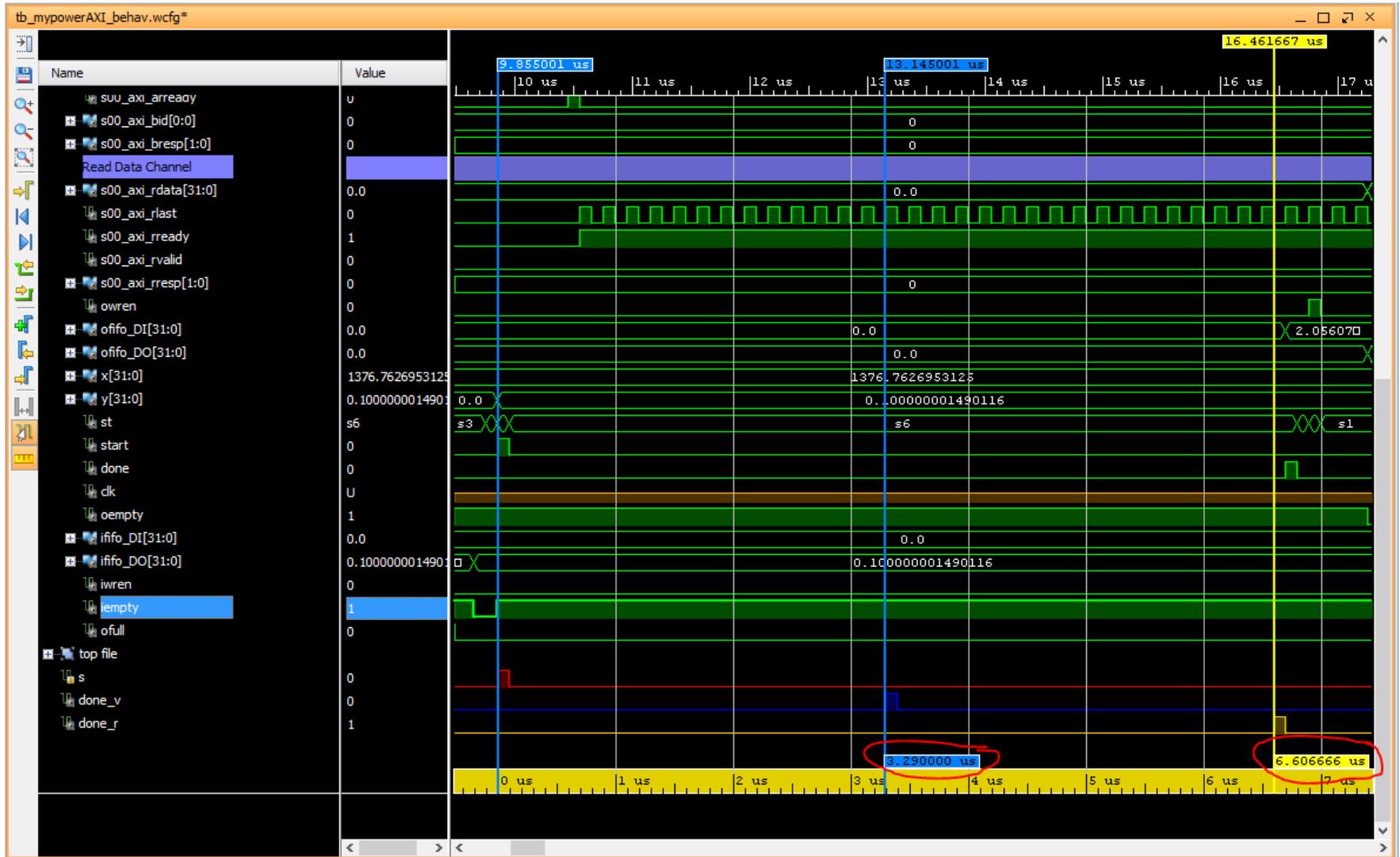


## Simulation with AXI



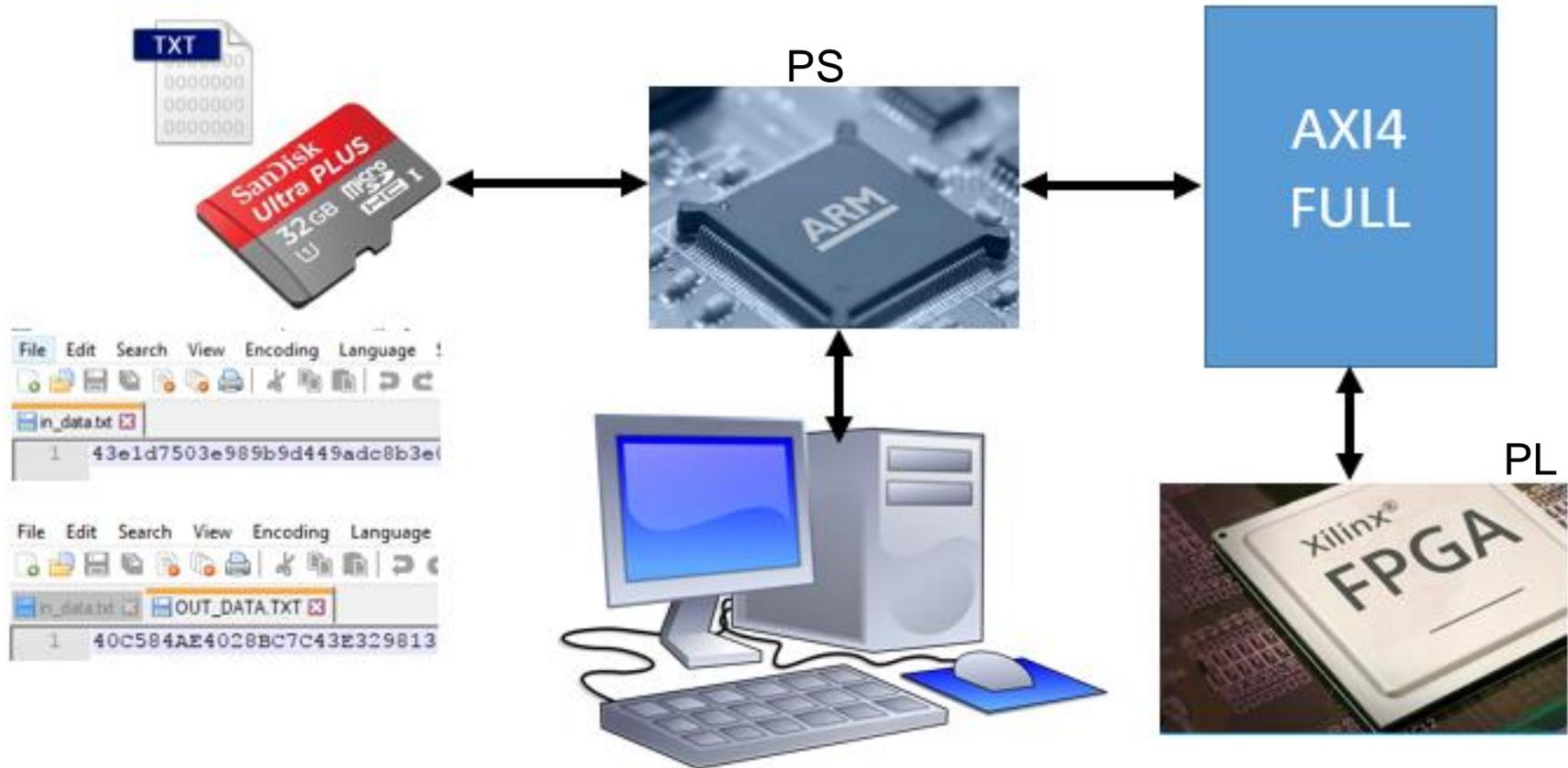


## Simulation





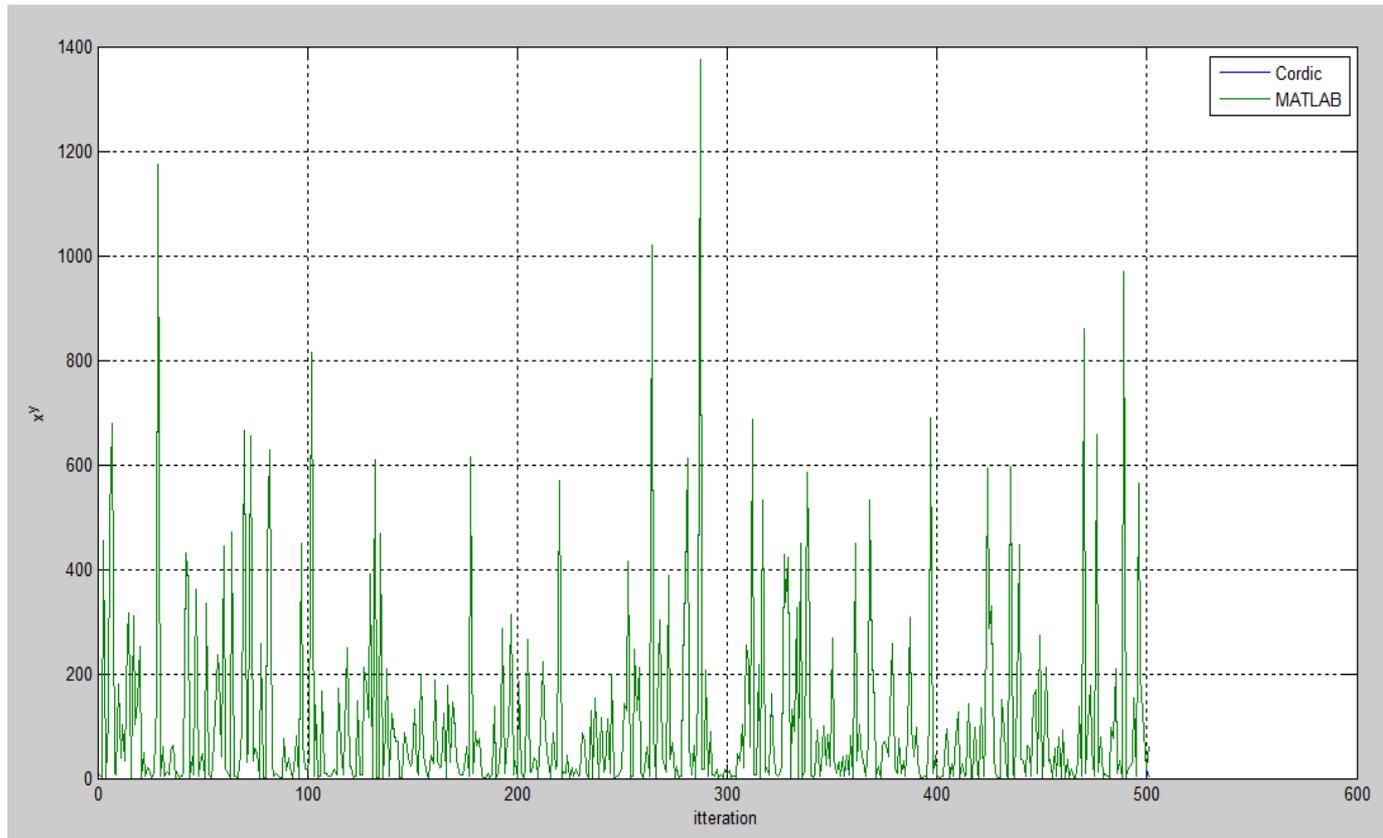
## Experimental Setup





## Results

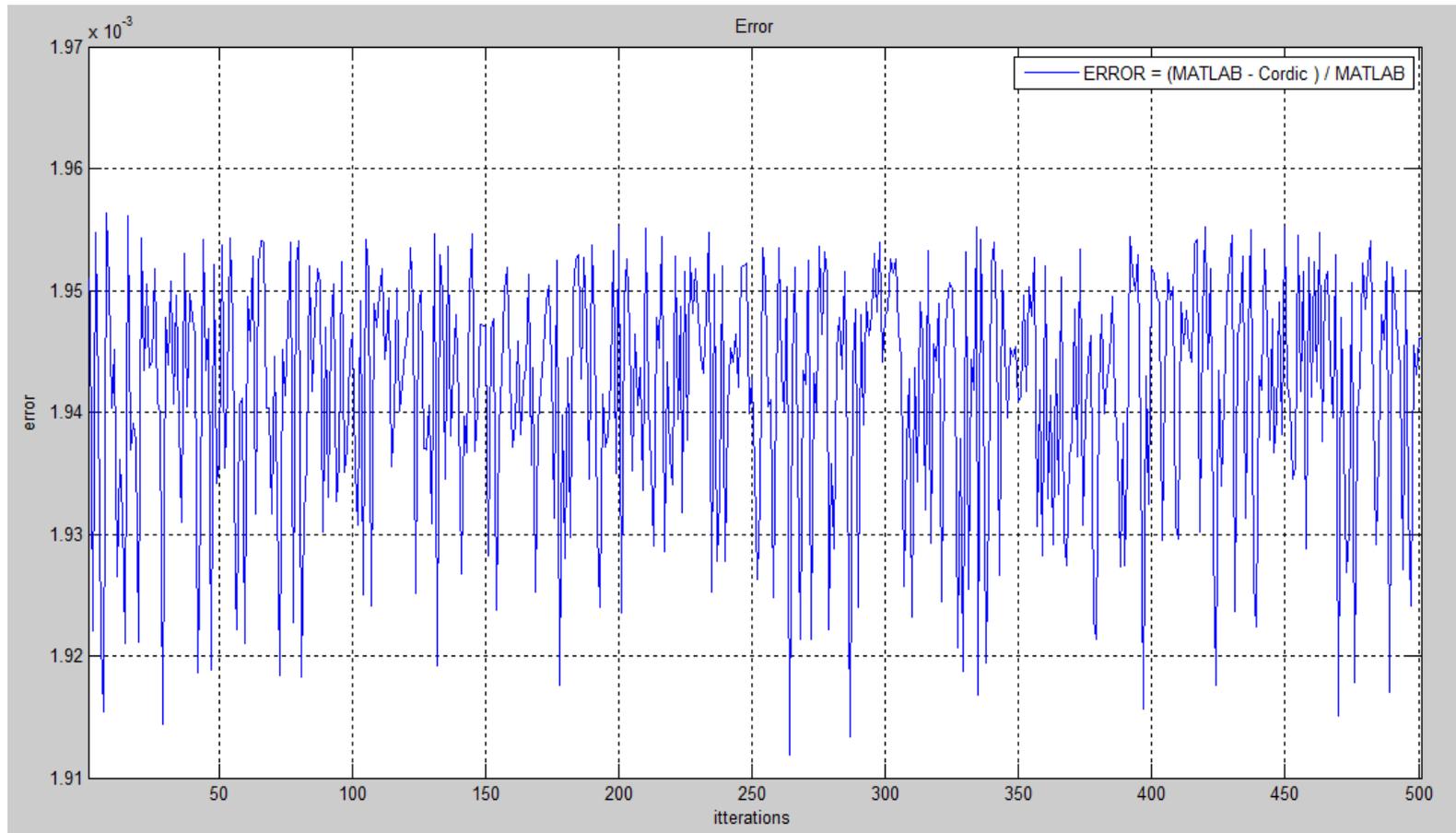
- Random variables were chosen for x and y (using rand function)
- $0 \leq x \leq 1500$  and  $0 \leq y \leq 1$
- 500 data set were generated





## Results

- The error relative error =  $(\text{MATLAB} - \text{Cordic}) / \text{MATLAB}$





# Demo



# Thank You

- Any Questions??